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| APPLICATION NO.               | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.  | CONFIRMATION NO. |
|-------------------------------|-------------|----------------------|----------------------|------------------|
| 10/034,528                    | 12/27/2001  | Benjamin N. Eldridge | P6C3-US              | 2563             |
| 50905                         | 7590        | 01/06/2006           | EXAMINER             |                  |
| N. KENNETH BURRASTON          |             |                      | KOBERT, RUSSELL MARC |                  |
| KIRTON & MCCONKIE             |             |                      |                      |                  |
| P.O. BOX 45120                |             |                      | ART UNIT             |                  |
| SALT LAKE CITY, UT 84145-0120 |             |                      | 2829                 |                  |
|                               |             |                      | PAPER NUMBER         |                  |

DATE MAILED: 01/06/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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|                              |                                      |  |  |
|------------------------------|--------------------------------------|--|--|
| <b>Office Action Summary</b> | <b>Application No.</b><br>10/034,528 | <b>Applicant(s)</b><br>ELDRIDGE ET AL. |  |
|                              | <b>Examiner</b><br>Russell M. Kobert | <b>Art Unit</b><br>2829                |  |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 03 October 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 43,48,49,51-57,59-65 and 74-101 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 43,48,49,51-57,59-65 and 74-101 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>1104 &amp; 1005</u> . | 6) <input type="checkbox"/> Other: _____  |

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1. Applicants are hereby notified that the Letter titled "Notice of Improper Request For Continued Examination (RCE)" mailed on 3 November 2005 is hereby withdrawn. All issues presented therein should be disregarded.

2. Further to the above, Applicants are hereby notified that the RCE filed 3 October 2005 has been re-entered.

3. The details of the prior Office Action mailed on December 27, 2001 are reiterated as follows in response to the RCE filed October 3, 2005.

4. Applicant's arguments, see page 11, lines 10-14, that asserts the subject matter of each of the claims is expressly directed to "a tested semiconductor device," and "not a probe card assembly or a combination of a probe card assembly and a tested semiconductor device", filed November 12, 2004, with respect to the rejection under 35 USC 112 has been fully considered and is persuasive. Moreover Applicant's arguments, see page 12, lines 8-14, that asserts the "product -- that is, the tested semiconductor device -- patentably differs from prior art semiconductor devices," filed November 12, 2004, with respect to the rejection under 35 USC 112 has been fully considered and is persuasive. Additionally, Applicant's arguments, see page 11, lines 15-22, that asserts "Applicants are not attempting to do what is forbidden Ex parte Lyell, 17 USPQ 1548 (Bd. Pat. App. & Inter. 1990), which was cited in the Office Action" and further states "the preambles of the claims in the present application are expressly

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directed only to a 'tested semiconductor device'," filed November 12, 2004, with respect to the rejection under 35 USC 112 have been fully considered and are persuasive. The rejection under 35 USC 112 of claims 43, 48, 49, 51-57, 59-65, 68 and 74-83 has been withdrawn.

In summary, **Applicants' claimed invention is limited to only a tested semiconductor device and nothing more than a tested semiconductor device**; no patentable weight being given to the process of producing the test semiconductor device and no patentable weight given to a probe card assembly including any details of the probe card assembly in view of Applicants' own admission in the Amendment filed November 12, 2004.

5. Applicant's most recent arguments with respect to claims 43, 48, 49, 51-57, 59-65 and 74-101 have been considered but are moot in view of the new ground(s) of rejection.

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

7. Claims 43, 48, 49, 51-57, 59-65 and 74-101 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Littlebury (5012187).

Littlebury anticipates (Figure 2) a tested semiconductor device (semiconductor chips 12 tested in wafer form and reference made to processing memory chips 12 **after testing**; see col 2, ln 26-29 and col 4, ln 40-63) produced by providing a wafer (col 4, ln 11-16; Littlebury states the preferred embodiment being in wafer form) having a plurality of semiconductor devices (12) thereon, each of the semiconductor devices including a plurality of electrical contact terminals (13) as recited in claims 43 and 82.

As to claim 48, dicing the wafer to singulate the semiconductor devices is anticipated by Littlebury (col 4, ln 11-18).

Littlebury anticipates the limitations of claims 49, 51-57, 59-65 and 74-81 and 83-101, because the additional limitations presented in each of claims 49, 51-57, 59-65 and 74-81 and 83-101 do not add patentable weight to the claimed invention and do not further narrow the scope of Applicants' claimed invention that is directed solely to "a tested semiconductor device."

8. A shortened statutory period for response to this action is set to expire three month(s) from the date of this letter. Failure to respond within the period for response will cause the application to become abandoned. 35 U.S.C. 133

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Russell Kobert whose telephone number is (571) 272-1963. For an automated menu of Tech Center 2800 phone numbers call (571) 272-2800.



Russell M. Kobert  
Patent Examiner  
Group Art Unit 2829  
January 3, 2006

  
**PARESH PATEL** 01/05/06  
**PRIMARY EXAMINER**